IN THE U.S. PATENT AND TRADEMARK OFFICE

June 13, 2002

Applicant(s): Chikao IKENAGA, et al.

For: FRAME FOR SEMICONDUCTOR PACKAGE

Serial No.: 09/850 213 Group: 2826

Confirmation No.: 9610

Filed: May 7, 2001 Examiner: Greene

International Application No.: -

International Filing Date: Atty. Docket No.: OPS C-533

Assistant Commissioner for Patents Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In compliance with the provisions of Rules 1.97(c)(2) and 1.98, Applicants respectfully request consideration of the prior art enclosed herewith and listed on the enclosed Form PTO-1449. A check in the amount of \$180.00 is enclosed to cover the Rule 97(c) fee.

Also listed on Form PTO-1449 is co-pending Application Serial No. 09/850 212 filed on May 7, 2001 and entitled "Frame for Semiconductor Package". The '212 application is owned by the same assignee of the instant application. A copy of the '212 application is enclosed for the Examiner's review.

The currently pending claims are believed to patentably distinguish over the listed references, and further comment at this point in time is not believed necessary.

The Commissioner is hereby authorized to charge any additional fees which may be required by this paper, or credit any overpayment, to Deposit Account No. 06-1382. A duplicate copy of this sheet is enclosed.

Further consideration is respectfully solicited.

Respectfully submitted,

IN DUPLICATE

LLC/ad

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Encl: Form PTO-1449

One copy of each cited reference

Copy of U.S. Application Serial No. 09/850 212

including specification, claims and drawings and

Amended Claims 1-4 and new Claims 5-17

136.0112



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